SMARC 2.0

Engicam introduces the new ***SmarCore APL-x86 SOM*** based on Low Power Intel® Atom™, Celeron® and Pentium® processors (Formerly Apollo Lake). The new module will be based on the SMARC standard pinout.

SmarCore APL-x86

Windows 10

Windows IoT ENTERPRISE

Windows IoT CORE

yocto PROJECT

SMARC

PROCESSOR AND MEMORY

CPU	Intel® Atom™ x7-E3950 (4 x 1.6 / 2.0 GHz, 2MB L2 cache, 12W) Intel® Atom™ x5-E3940 (4 x 1.6 / 1.8 GHz, L2 cache 2MB, 9W) Intel® Atom™ x5-E3930 (2 x 1.3 / 1.8 GHz, L2 cache 1MB, 6.5W) Intel® Celeron® N3350 (2 x 1.1 / 2.4 GHz, L2 cache 1MB, 6W) Intel® Pentium® N4200 (4 x 1.1 / 2.5 GHz, L2 cache 2MB, 6W)
MAX CORES	4
MAX THREAD	4
RAM	On board up to 8GB DDR3L-1866
eMMC	8GB eMMC 5.0 expandible
SATA	1 x Sata Gen3
SD	1 x 4 bit

MECHANICAL

Standard SMARC™ 2.0 short size module

Preliminary subject to changes

DISPLAY AND GRAPHICS

ENGINE	Intel® 9th generation (Gen 9) LP graphics and media encode/decode engine
HDMI/ DP	Dual channel up to 1920x1080@60Hz via eDP bridge
eDP	4K available without LVDS
MIPI-CSI	1 x 4 lane and 1 x 2 lane

I/O

POWER SUPPLY	5Vdc single supply
USB	2 x USB3.0 4 x USB2.0
PCIe	up to 4 x PCIe Gen2
ETHERNET	1x Intel® I210 (Industrial)
AUDIO	1 x HDA and 1 x I2S
OTHERS	I2C, SPI, UART, SDIO, GPIO

QUALIFICATION AND LIFETIME

Industrial and consumer qualified

10 and 15-year Longevity Program

Microtron®
Flexible electronic solutions

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